

Weekly Report

12/28/2015-1/03/2016

Work

This week, I have completed computer architecture homework and read some papers [2, 3, 4, 1, 5]. Besides, I am invited to process trajectory data for Wu Feiran's phd thesis recently. Next week, I plan to finish other coursework and process data with Ma Yuxin as soon as possible.

Plan for next week

- Complete other coursework
- Process trajectory data

References

- [1] Jayshree Ghorpade, Jitendra Parande, Madhura Kulkarni, and Amit Bawaskar. Gpgpu processing in cuda architecture. arXiv preprint arXiv:1202.4347, 2012.
- [2] Youngsok Kim, Jaewon Lee, Jae-Eon Jo, and Jangwoo Kim. Gpudmm: A high-performance and memory-oblivious gpu architecture using dynamic memory management. In High Performance Computer Architecture (HPCA), 2014 IEEE 20th International Symposium on, pages 546 – 557. IEEE, 2014.
- [3] Cedric Nugteren, Gert-Jan van den Braak, Henk Corporaal, and Henri Bal. A detailed gpu cache model based on reuse distance theory. In High Performance Computer Architecture (HPCA), 2014 IEEE 20th International Symposium on, pages 37 – 48. IEEE, 2014.

- [4] John D Owens, David Luebke, Naga Govindaraju, Mark Harris, Jens Krüger, Aaron E Lefohn, and Timothy J Purcell. A survey of general-purpose computation on graphics hardware. In Computer graphics forum, volume 26, pages 80 – 113. Wiley Online Library, 2007.
- [5] 袁晓如 陆旻. 地理空间数据可视化中的过滤. 计算机辅助设计与图形学学报, 2015.